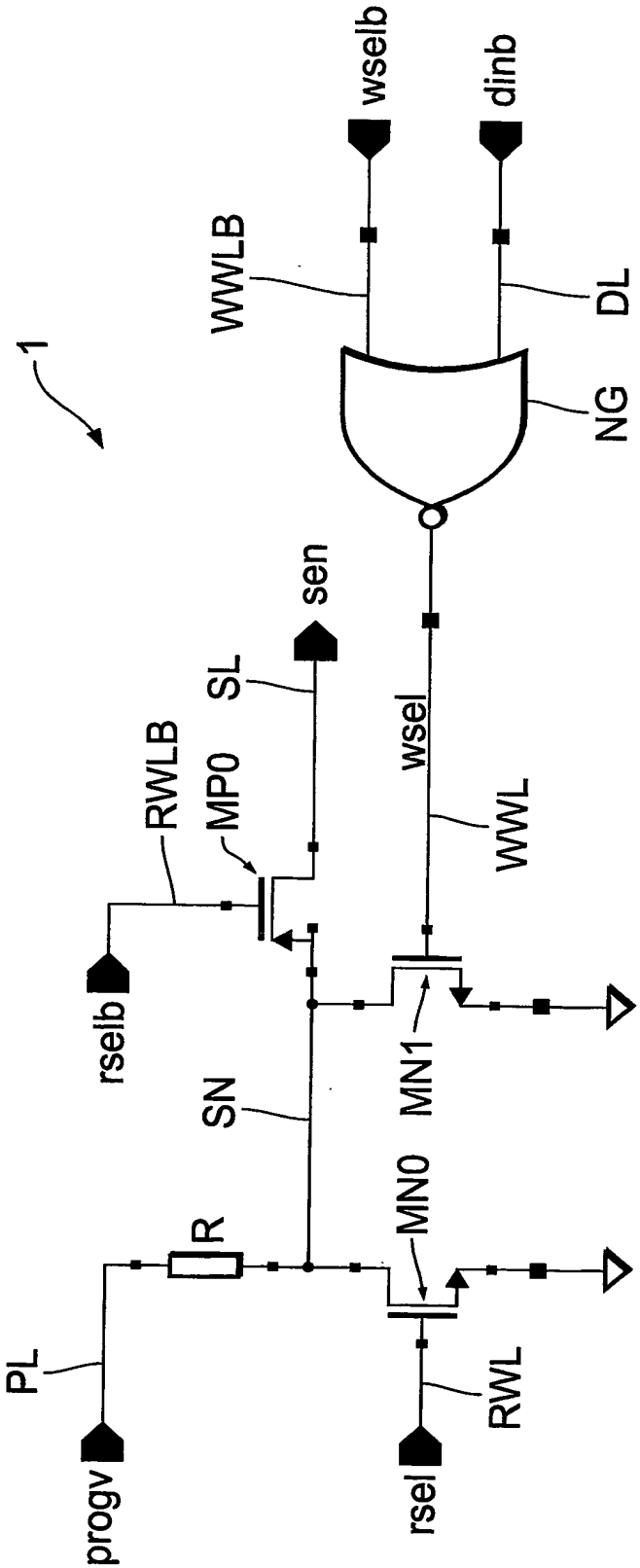

wselb
dinb
WWLB
DL
1
NG
sen
SL
wsel
RWLB
MP0
WWL
rselb
MN1
SN
MN0
R
PL
RWL
progv
rsel

FIG.1

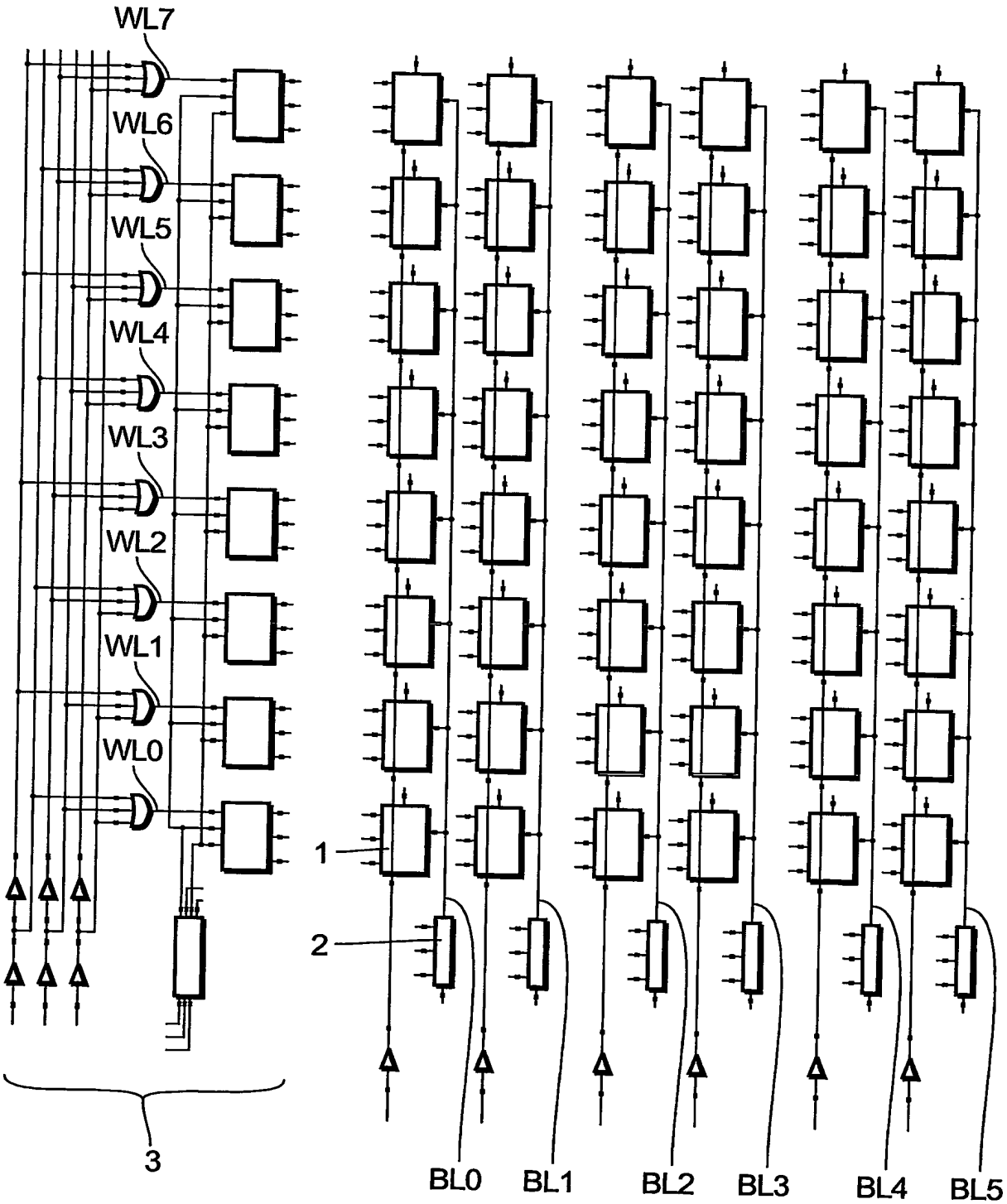


FIG.2

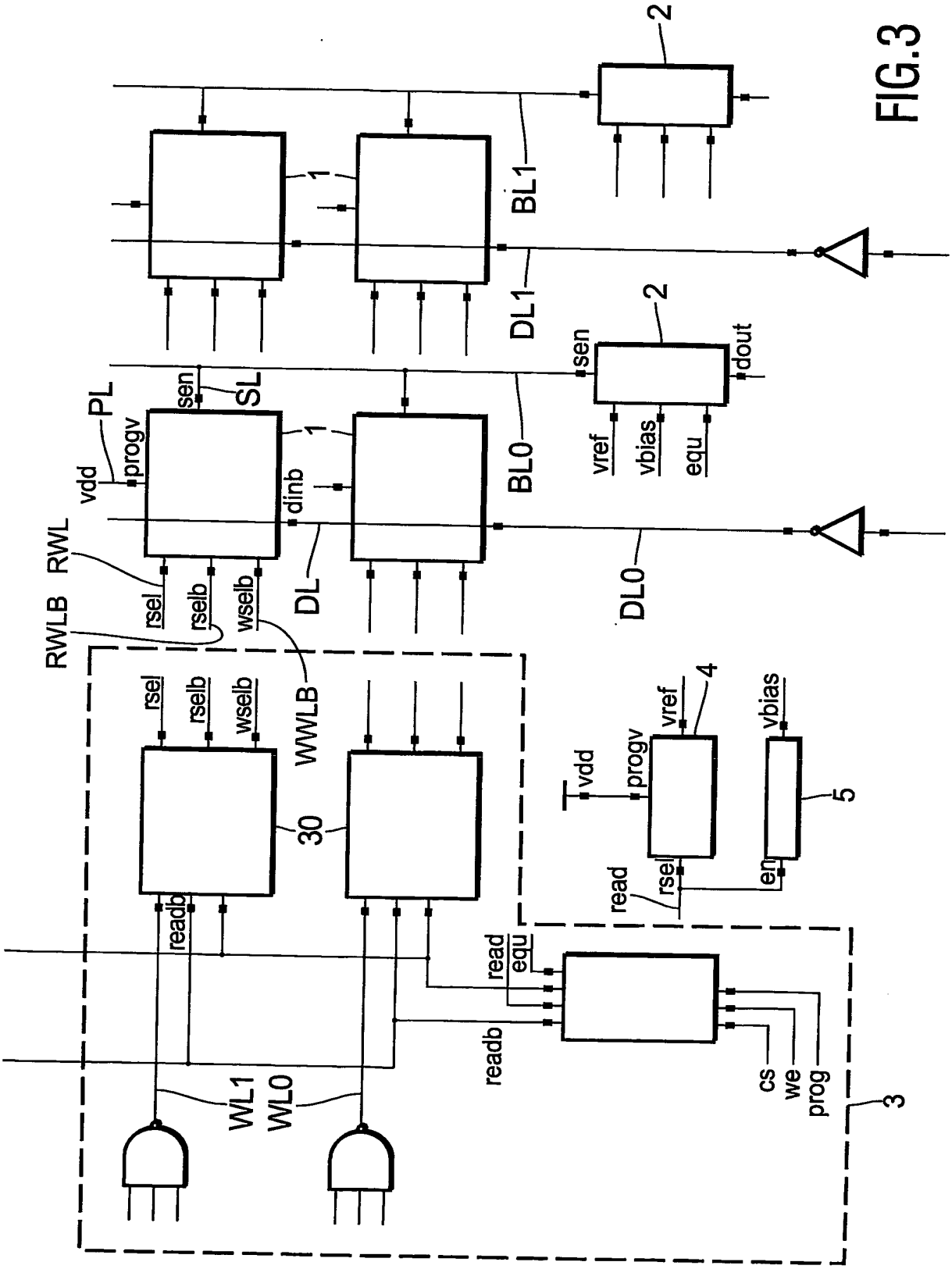

2
BL1
1
DL1
2
sen
dout
PL
sen
SL
progv
vdd
1
dinb
BL0
vref
vbias
equ
RWL
rsel
rselb
wselb
DL
DL0
RWLB
WWLB
rsel
rselb
wselb
30
vdd
progv
vref
4
vbias
5
readb
read
rsel
en
read
equ
readb
cs
we
prog
3
WL1
WL0

FIG.3

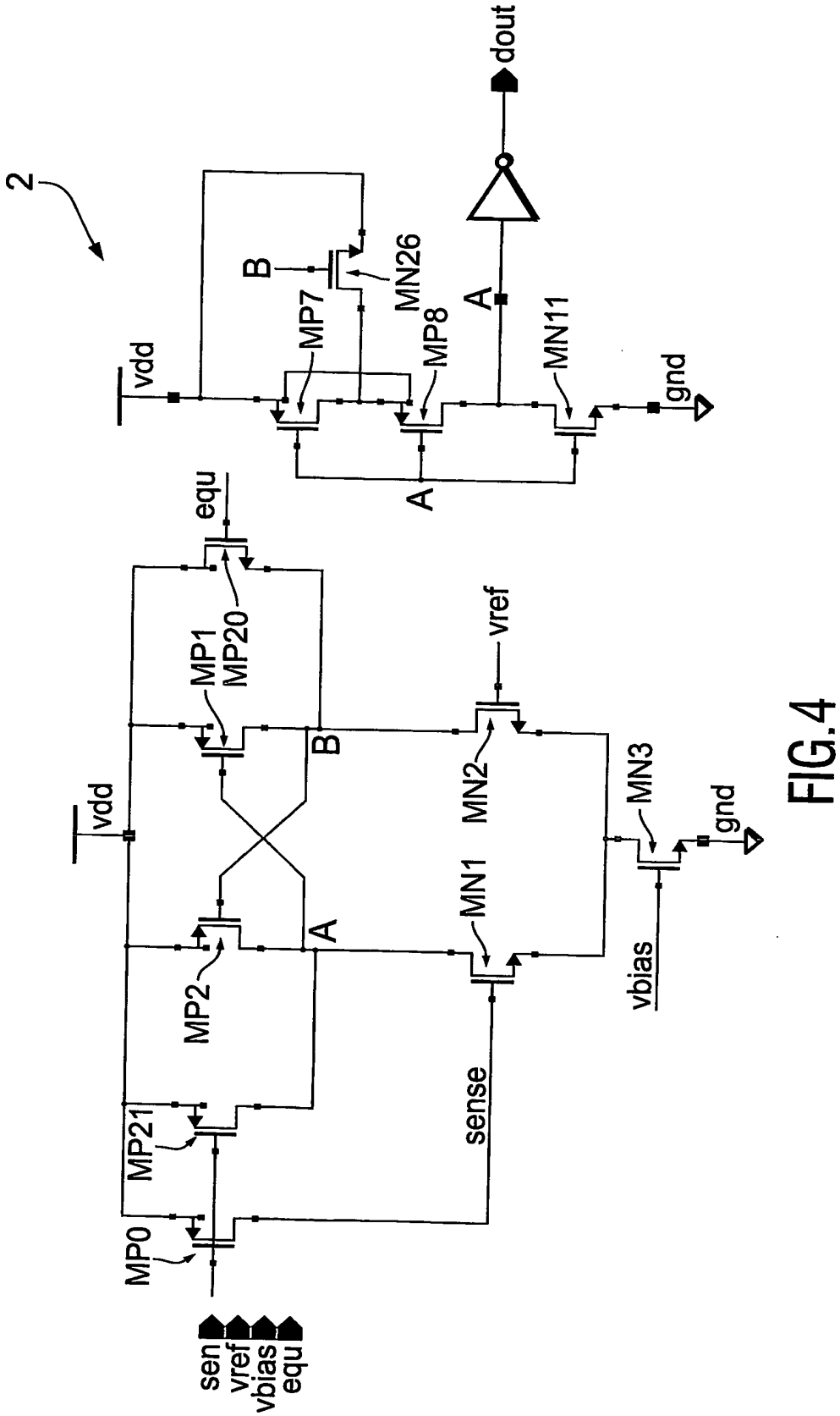

2
dout
B
MN26
MP7
MP8
A
MN11
vdd
gnd
A
equ
MP1
MP20
vref
B
MN2
MN3
vdd
gnd
MN1
A
MP2
vbias
sense
MP21
MP0
sen
vref
vbias
equ

FIG.4

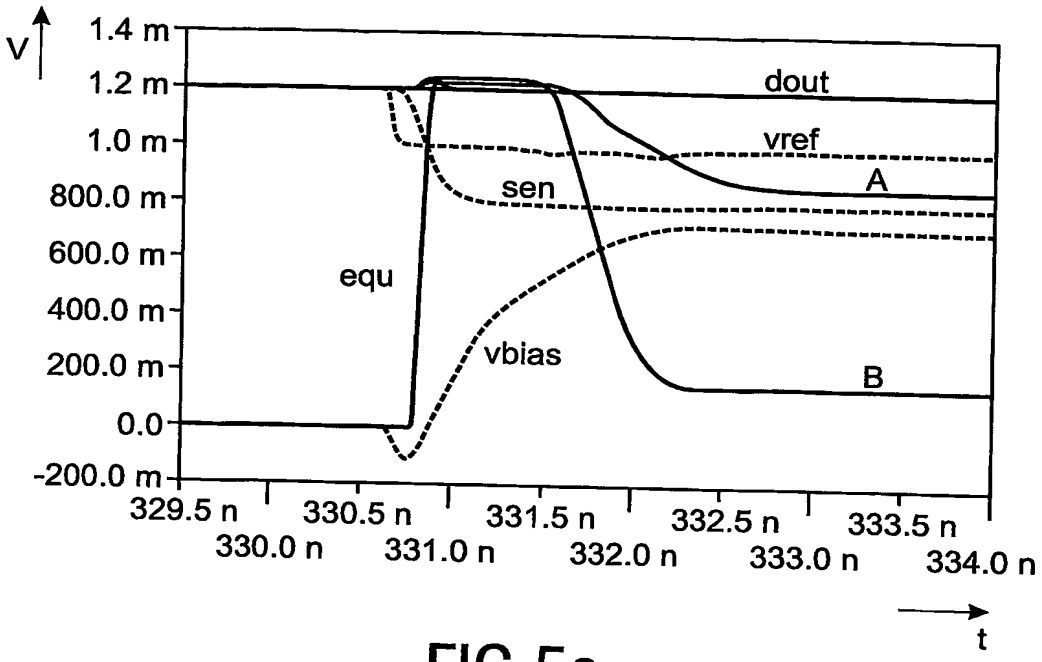

V
1.4 m
1.2 m
1.0 m
800.0 m
600.0 m
400.0 m
200.0 m
0.0
-200.0 m
dout
vref
sen
A
equ
vbias
B
329.5 n
330.0 n
330.5 n
331.0 n
331.5 n
332.0 n
332.5 n
333.0 n
333.5 n
334.0 n
t

FIG.5a

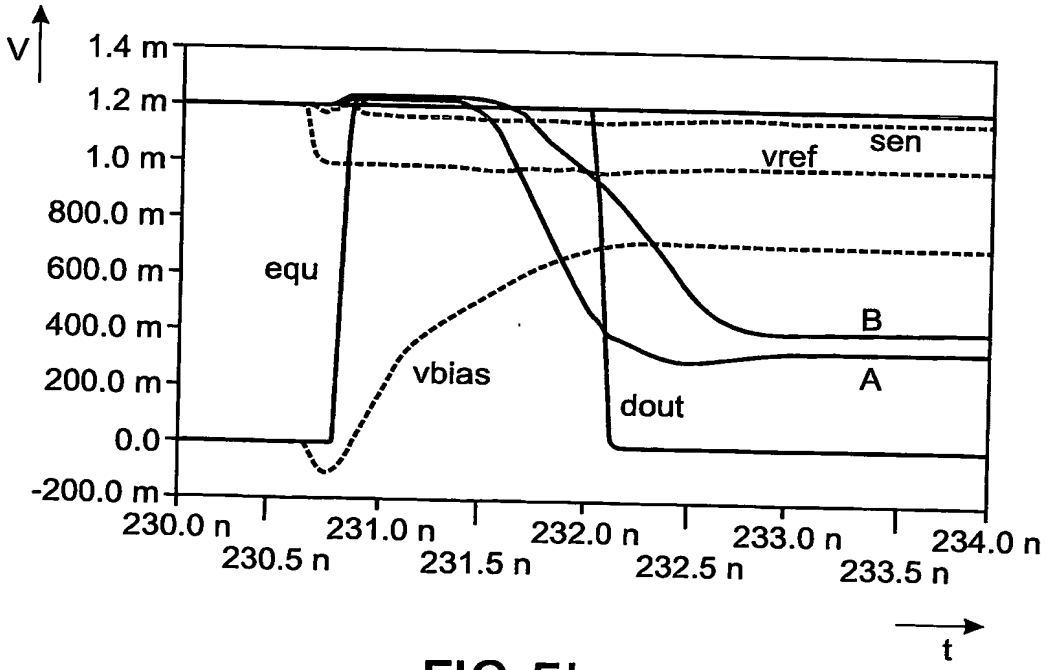

V
1.4 m
1.2 m
1.0 m
800.0 m
600.0 m
400.0 m
200.0 m
0.0
-200.0 m
sen
vref
equ
B
vbias
A
dout
230.0 n
230.5 n
231.0 n
231.5 n
232.0 n
232.5 n
233.0 n
233.5 n
234.0 n
t

FIG.5b

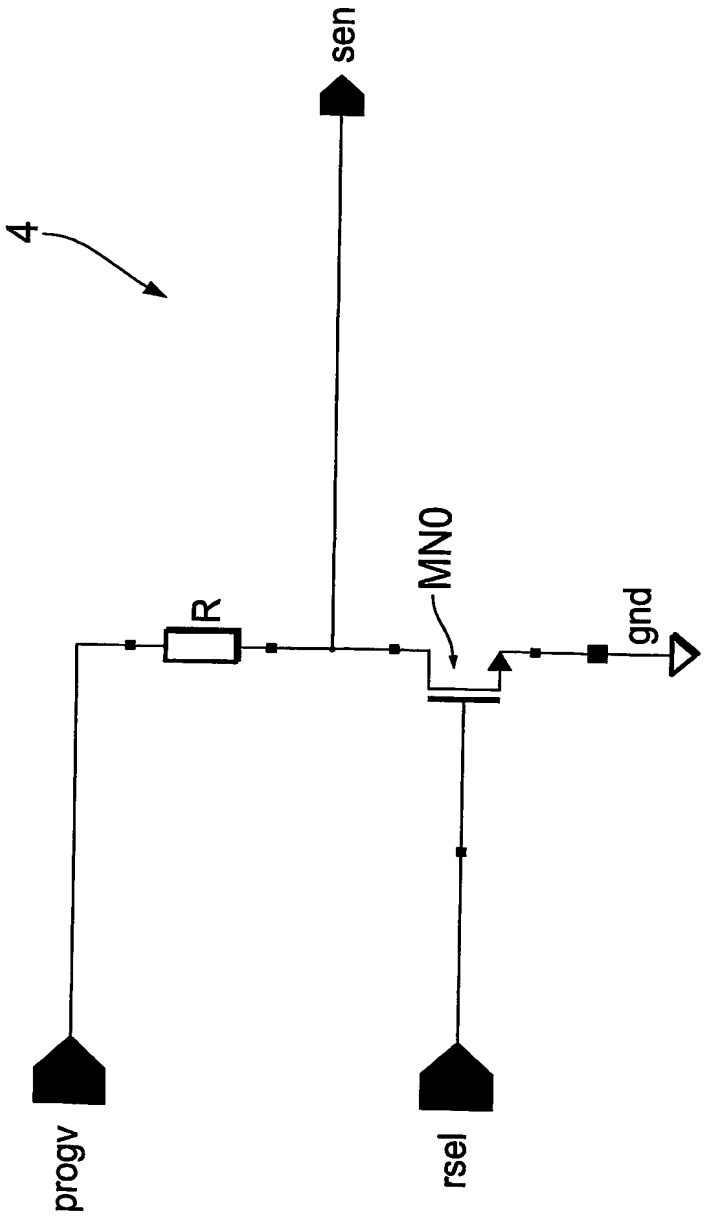


FIG.6

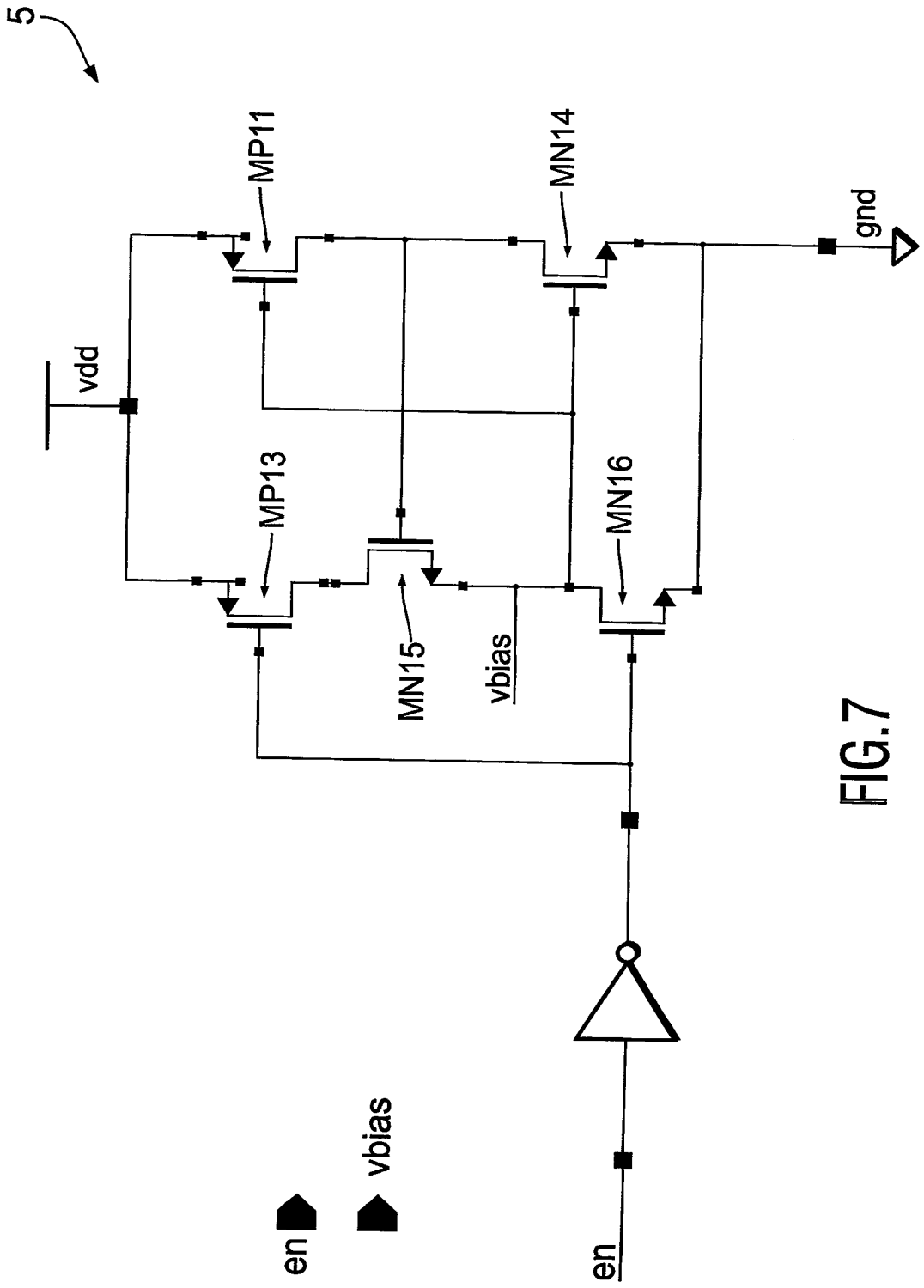


FIG.7

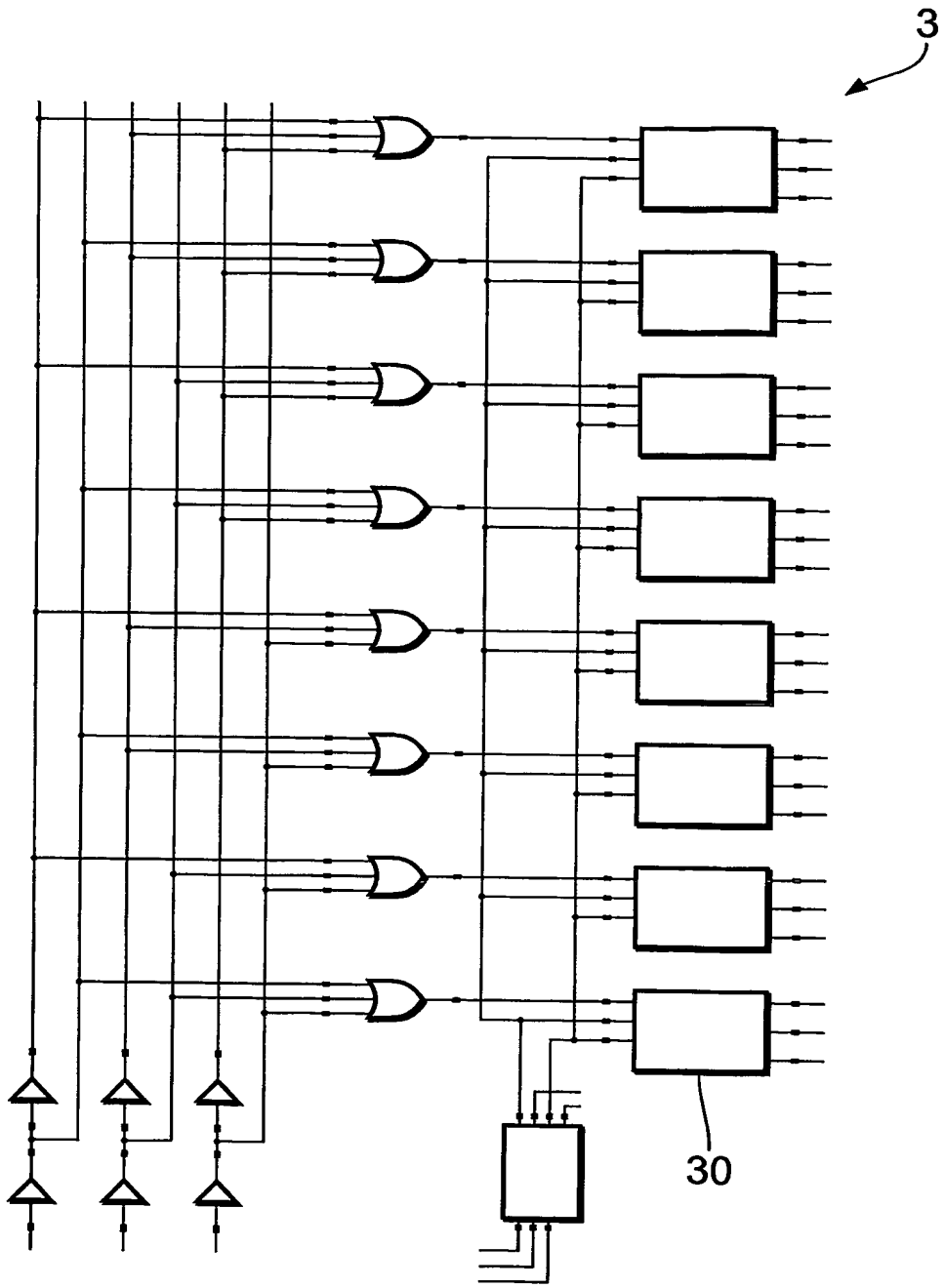


FIG.8

FIG.9a

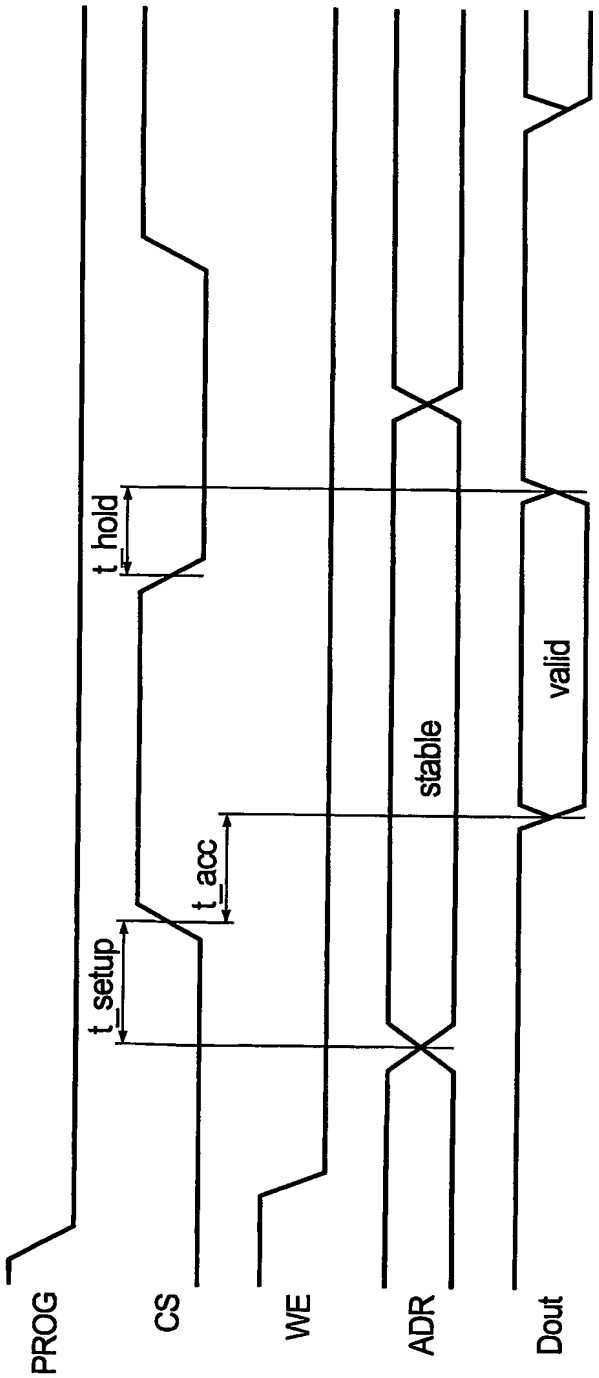
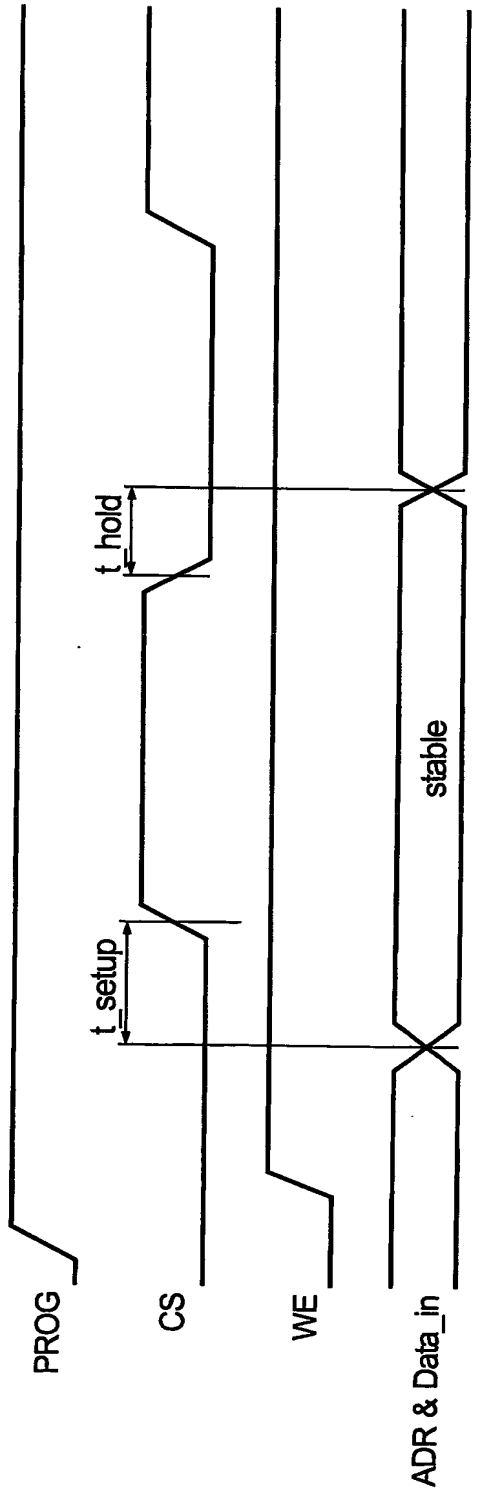

PROG
CS
WE
ADR
Dout
t_hold
t_acc
t_setup
stable
valid

FIG.9b


PROG
CS
WE
ADR & Data_in
t_hold
t_setup
stable